

(2)

Unit-II

2. (a) Discuss De Morgan's law with suitable example.
- (b) What is Flip Flop ? Explain JK Flip Flop with circuit diagram.
- (c) What is Full Adder ? Draw circuit diagram of Full Adder.

Unit-III

3. (a) Explain the following :
- (i) Registers
- (ii) System buses
- (b) Draw and explain block diagram of micro computer.
- (c) Explain interfacing devices.

Unit-IV

4. (a) Differentiate between synchronous and asynchronous data transfer.
- (b) Discuss about I/O Interface.
- (c) Explain I/O devices and their controllers.

Unit-V

5. (a) Explain the following :
- (i) Hit Ratio
- (ii) Virtual Memory

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- (b) Discuss about semiconductor memory.
 - (c) Explain memory hierarchy and associative memory.
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